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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
Substitute for form 1449A/PTO		Application Number	N/A
		Filing Date	Herewith
		First Named Inventor	Paul Gerard D'Arcy
		Art Unit	N/A
		Examiner Name	N/A
Sheet	1	of	2
		Attorney Docket Number D'ARCY 15-6-7	

Examiner Signature	R. Stephen Oldsine	Date Considered	5 January 2006
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Substitute for form 1449B/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	N/A
(Use as many sheets as necessary)				Filing Date	Herewith
				First Named Inventor	Paul Gerard D'Arcy
				Art Unit	N/A
				Examiner Name	N/A
Sheet	2	of	2	Attorney Docket Number	D'ARCY 15-6-7

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
RSO		C. Perkins; Minimal Encapsulation Within IP; RFC 2004; October 1996.			
RSO		K. Bickerstaff, et al.; Parallel Reduced Area Multipliers; Journal of VLSI Signal Processing; Vol. 9; Kluwer Academic Publishers, Boston; 1995; pp. 181-192.			
RSO		I. Koren; Computer Arithmetic Algorithms; Prentice-Hall, Inc.; 1993; pp. 71-72 and pp. 108-111.			
RSO		J. Postel; Internet Protocol; RFC 791; University of Southern California, Information Sciences Institute; September 1981; pp. 1-45.			
RSO		J. Postel; Transmission Control Protocol; RFC 793; University of Southern California, Information Sciences Institute; September 1981; pp. 1-85.			
RSO		J. Postel; User Datagram Protocol; RFC 768; August 1980; pp. 1-3.			
RSO		L. Dadda; Some Schemes For Parallel Multipliers; Alta Frequenza; Vol. 34; pp. 349-356; 1965.			
RSO		C.S. Wallace; A Suggestion For a Fast Multiplier; IEEE Transactions on Electronic Computers; February 1964; pp. 14-17.			

Examiner Signature	<i>R. Stephen Dulcine</i>	Date Considered	5 January 2006
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